# MarieSimR: The MARIE Computer Simulator Revision

Xuejun Liang Department of Computer Science California State University - Stanislaus Fall 2021

## Introduction

- MarieSim is the MARIE computer simulator
  - MARIE is an accumulator-based computer model used in the popular textbook "The essentials of computer organization and architecture".
  - Used for assembly language programming exercises.
- But, MarieSim is too simple and thus unable to support some important concepts in computer architecture:
  - No immediate addressing mode
  - No stack and thus its subroutine has no local variables and can not be recursive.
  - Can not define a variable to hold the address of another variable symbolically.
- In order to solve these problems, a revision to MarieSim, called MarieSimR, is developed.

# Outline

- Design and Implementation of MarieSimR
- Assembly Language Program Examples
  - to illustrate how to use MarieSimR
  - to compare MarieSim and MarieSimR

# The Goal of This Revision

- The goal of this work is to revise MARIE to support the stack and recursive subroutines without changing its architectural characteristics and its microarchitecture for control unit
- The MARIE architecture has the following characteristics:
  - Binary, two's complement data representation.
  - Stored program, fixed word length data and instructions.
  - 4K words of word-addressable main memory.
  - 16-bit data words.
  - 16-bit instructions, 4 for the opcode and 12 for the address.
  - A 16-bit arithmetic logic unit (ALU).
  - Seven registers for control and data movement.

## MarieSimR: What's New?

- The stack pointer stored in a reserved memory location and the stack-relative addressing mode are added.
- The subroutine call and return instructions are revised to use the stack for the subroutine return address.
  - A stack frame can be created for a subroutine to hold the return address, input arguments, output results, local variables and so on. So, recursive subroutines are supported.
- A new instruction for increasing or decreasing the value of the stack pointer is added to facilitate the push and pop operations.
- A new instruction for loading an immediate constant into the accumulator is added to replace the clear instruction.
- A new assembler directive is added to support for defining a label to hold the address of another label symbolically

## MARIE Instruction Set and Revision

Opcode	Instruction	Meaning
0000	JnS X	$Mem[X] \leftarrow PC \& PC \leftarrow X+1$
	Call X	Push PC & PC 🗲 X
0001	Load X	AC ← Mem[X]
0010	Store X	$Mem[X] \leftarrow AC$
0011	Add X	$AC \leftarrow AC + Mem[X]$
0100	Subt X	$AC \leftarrow AC - Mem[X]$
0101	Input	AC $\leftarrow$ value from keyboard
0110	Output	Display value in AC on screen
0111	Halt	Terminate program
1000	Skipcond 000	Skip next instruction if AC < 0
	Skipcond 400	Skip next instruction if AC = 0
	Skipcond 800	Skip next instruction if AC > 0
1001	Jump X	PC ← X
1010	Clear	AC ← 0
	Limm Imm	AC ← Imm
1011	AddI X	$AC \leftarrow AC + Mem[Mem[X]]$
1100	Jumpl X	$PC \leftarrow Mem[X]$
	JR	POP PC
1101	LoadI X	$AC \leftarrow Mem[Mem[X]]$
1110	Storel X	$Mem[Mem[X]] \leftarrow AC$
1111	IncSP Imm	Mem[SP] += Imm

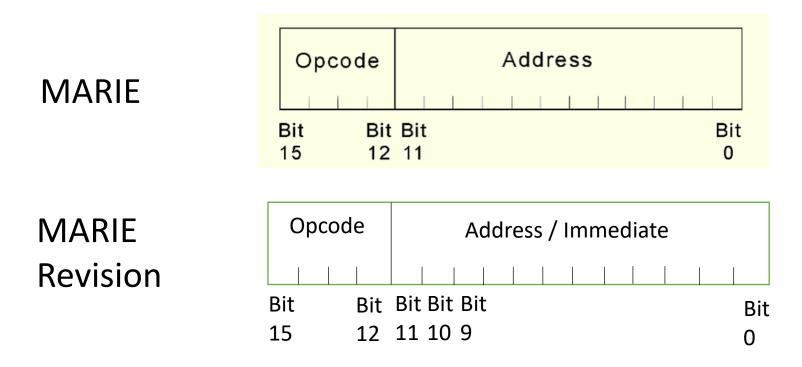
- X is either a hexadecimal literal or a label (symbol) and is used as a memory address
- Mem[X] represents the content at the memory location X.
- Imm is a 12-bit decimal constant integer.
- When using stack-relative addressing, X has the format: \$±offset
  - \$ represents the value of the stack pointer
  - Offset is a 10-bit decimal constant integer.

# MARIE Memory Map with Stack

The MARIE architecture has 4K words of word- addressable main memory. The stack grows towards		0	Program and data
the high memory address end and starts from 3072.	2	070	Region (3K-1)
The stack occupies 1K out of 4K MARIE word-	_	071	MEM[SP]
addressable memory space.	3	072	Stack
The stack pointer is located at memory	4	095	Region (1k)

location 3071

# Instruction Encoding Format



Instructions Limm Imm and IncSP Imm store the 12-bit integer in 2's complement inside the instruction from Bit 0 to Bit 11.

If Bit 11 is 1 and Bit 10 is 1, then the stack-relative addressing is used and the offset address is stored inside the instruction from Bit 0 to Bit 9.

# Push and Pop Operations

Push X	Meaning
IncSP 1	$Mem[SP] \leftarrow Mem[SP] + 1$
Load X	$AC \leftarrow Mem[X]$
Storel SP	Mem[Mem[SP]] ← AC

Рор Х	Meaning		
loadI SP	AC ← Mem[Mem[SP]]		
Store X	$Mem[X] \leftarrow AC$		
IncSP -1	$Mem[SP] \leftarrow Mem[SP] - 1$		

The instruction IncSP Imm is not necessary as It can be replaced by three other instructions

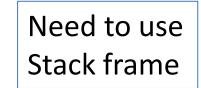


# MARIE Directives and Extension

- Directives are instructions to assemblers.
- MarieSim has five directives
  - ORG defines the starting address of the program.
  - DEC, OCT, and HEX define named constant in decimal, octa-decimal, and hexadecimal, respectively.
  - END indicates the end of the program.
- MarieSimR adds one more directive LAB
  - It defines a named hexadecimal constant specified either by a hexadecimal literal or a label symbolically.

# Two Programming Examples

- Example 1: Compute sum of numbers in an array.
  - Loop through an array
  - Compare MarieSim and MarieSimR
- Example 2: Compute Fibonacci number Fib(N)
  - Using Loop
  - Using Subroutine with global variables
  - Using Subroutine with local variables
  - Using Recursive subroutine



### Example 1: Using loop to add five numbers in array, save result to Sum

/ Cod	e for Mar:	ieSim a	nd MarieSimR	/ Code	e for Mar:	ieSimR
	ORG	100	/Program starts from 0x100		ORG	100
	Load	Addr	/Load address of first number to be added		Load	Addr
	Store	Next	/Store this address is our Next pointer		Store	Next
	Load	Num	/Load the number of items to be added		Limm	-1
	Subt	One	/Decrement		Add	Num
	Store	Ctr	/Store this value in Ctr to control looping		Store	Ctr
Loop,	Load	Sum	/Load the Sum into AC	Loop,	Load	Sum
	AddI	Next	/Add the value pointed to by location Next		AddI	Next
	Store	Sum	/Store this sum		Store	Sum
	Load	Next	/Load Next		Limm	1
	Add	One	/Increment by one to point to next address		Add	Next
	Store	Next	/Store in our pointer Next		Store	Next
	Load	Ctr	/Load the loop control variable		Limm	-1
	Subt	One	/Subtract one from the loop control variable		Add	Ctr
	Store	Ctr	/Store this new value in loop control variable		Store	Ctr
	Skipcond	000	/If control variable < 0, skip next instruction		Skipcond	000
	Jump	Loop	/Otherwise, go to Loop		Jump	Loop
	Halt		/Terminate program		Halt	
Addr,	Hex	117	/Numbers to be summed start at location 117	Addr,	Lab	Dat
Next,	Hex	0	/A pointer to the next number to add	Next,	Hex	0
Num,	Dec	5	/The number of values to add	Num,	Dec	5
Sum,	Dec	0	/The sum	Sum,	Dec	0
Ctr,	Hex	0	/The loop control variable	Ctr,	Hex	0
One,	Dec	1	/Used to increment and decrement by 1	Dat,	Dec	-10
Dat,	Dec	-10	/The values to be added together		Dec	15
	Dec	15			Dec	-20
	Dec	-20			Dec	25
	Dec	25			Dec	30
	Dec	30				
1						

# Example 2: Compute Fibonacci Number fib(N), Where N is an Input

 $\operatorname{fib}(N) = \begin{cases} N & \text{if } N < 2\\ \operatorname{fib}(N-1) + \operatorname{fib}(N-2) & \text{if } N \ge 2 \end{cases}$ Mathematics formula int I, A, B, C, N cin >> N; if (N < 2)C++ code using a loop C = N;else { A = 0; B = 1;C = fib(N)for (I = 2; I <= N; I++) { B = fib(N-1)C = B + A; A = B; B = C;A = fib(N-2)} } cout << C;</pre>

### Method 1 (Using Loop)

								_	
// /			n ar	nd MarieSimR	// (	Code for 1		nR	
	ORG	100				ORG	100		
	Input			Read input		Input			Read input
	Store	Ν		N=input		Store	N		N=input
	Store	С		C=N		Store	С		C=N
	Subt	I		AC = N-1		Subt	I		AC = N-1
	SKIPCOND	800	//	if N-1 > 0		SKIPCOND		//	if N-1 > 0
	JUMP L3					JUMP	L3		
L2,	Load	В		AC=B	L2,	Load	В		AC=B
	ADD	А		AC=B+A		ADD	A		AC=B+A
	Store	С		C=B+A		Store	С	•••	C=B+A
	Load	В	//	AC=B		Load	В	//	AC=B
	Store	Α		A=B		Store	Α		A=B
	Load	С		AC=C		Load	С		AC=C
	Store	В	//	B=C		Store	В	//	B=C
	Load	I	//	AC=1		LIMM	1	//	AC=1
	ADD	One	11	AC=I+1		ADD	I	//	AC=I+1
	Store	I	11	I=I+1		Store	I	//	I=I+1
	SUBT	Ν	//	AC=I-N		SUBT	Ν	//	AC=I-N
	SKIPCOND	400	11	if I=N, done		SKIPCOND	400	11	if I=N, done
	Jump	L2	11	otherwise, continue		Jump	L2	11	otherwise, continue
L3,	Load	С	11	print result	L3,	Load	С	11	print result
	Output					Output			
	Halt		11	stop		Halt		11	stop
11	Variable (	Declarat	tior	ns	1/ 1	Variable ເ	Declarat	tio	ns
Ι,	DEC	1	11	index	I,	DEC	1	11	index
Ν,	DEC	0	11	Ν	Ν,	DEC	0	11	Ν
с,	DEC	0	11	f(N)	c,	DEC	0	11	f(N)
В,	DEC	1		f(N-1)	В,	DEC	1		f(N-1)
Α,	DEC	0		f(N-2)	A,	DEC	0		f(N-2)
	, DEC	1		Used for increment by 1	-				
			· ·						

## Example 2: C++ Code with Using Function

```
int N, C;
int fib(int N) {
  int I, A, B, C;
  if (N < 2)
   C = N;
  else {
    A = 0; B = 1;
    for (I = 2; I <= N; I++) {
      C = B + A; A = B; B = C;
    }
  }
  return C;
}
cin >> N;
C = fib(N);
cout << C;</pre>
```

```
int N, C;
int fib(int N) {
  if (N < 2)
    return N;
  else
    return fib(N-1)+fib(N-2);
}
cin >> N;
C = fib(N);
cout << C;</pre>
        Recursive function
      Non-recursive function
```

### Method 2 (Using Function and Global Variables)

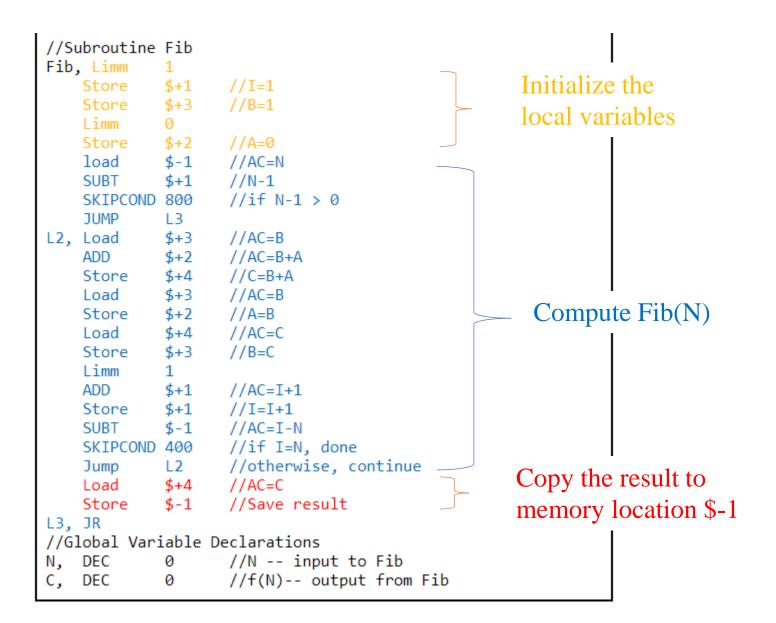
// Code for MarieSim ORG 100			// (	Code for I	MarieSi	mR	
			//Dead input		ORG 100		(/Dead input
	nput		//Read input		Input		//Read input
	tore	N	//N=input		Store	N	//N=input
	NS	Fib	//Call subroutine Fib		Call	Fib	//Call subroutine Fib
	oad	С	//Print result		Load	C	//Print result
	utput		<i>u</i>		Output		//_ · ·
	alt		//Terminate		Halt		//Terminate
	broutine				Subroutin		
Fib,		0	<pre>//for storing return addr</pre>	Fib	, Load	N	
	oad	Ν			Store	С	//C=N
		С	//C=N		Subt	I	//AC = N-1
	ubt	I	//AC = N-1		SKIPCOND		//if N-1 > 0
	KIPCOND		//if N-1 > 0		JUMP	L3	
J	UMP	L3		L2,	Load	В	//AC=B
L2, L	oad	В	//AC=B		ADD	А	//AC=B+A
A	DD	Α	//AC=B+A		Store	С	//C=B+A
S	tore	С	//C=B+A		Load	В	//AC=B
L	oad	В	//AC=B		Store	Α	//A=B
S	tore	Α	//A=B		Load	С	//AC=C
L	oad	С	//AC=C		Store	В	//B=C
S	tore	В	//B=C		Limm	1	//AC=1
L	oad	I	//AC=1		ADD	I	//AC=I+1
A	DD	One	//AC=I+1		Store	I	//I=I+1
S	tore	I	//I=I+1		SUBT	Ν	//AC=I-N
S	UBT	Ν	//AC=I-N		SKIPCOND	400	//if I=N, done
S	KIPCOND	400	//if I=N, done		Jump	L2	//otherwise, continue
J	ump	L2	//otherwise, continue	L3,	JR		
L3, J	umpI	Fib	-	11 0	Global Va	riable	Declarations
// Gl	obal Va	riable (	Declarations	I,	DEC	1	//index
I, D	EC	1	//index	Ν,	DEC	0	//N input to Fib
-	EC	0	//N input to Fib	c,	DEC	0	<pre>//f(N) output from Fib</pre>
	EC	0	//f(N) output from Fib	в,	DEC	1	//f(N-1)
	EC	1	//f(N-1)	Α,	DEC	0	//f(N-2)
-	EC	0	//f(N-2)				
One,		1	//used for increment by 1				
		_	······································				

## Method 3 (Using Function and Local Variables)

Stack frame of Fib(N)

Location in Stack	Memory Address	Used for
\$-1	Mem[SP]-1	Input N / Output Fib(N)
\$	Mem[SP]	Return Address
\$+1	Mem[SP]+1	Local Variable I
\$+2	Mem[SP]+2	Local Variable A
\$+3	Mem[SP]+3	Local Variable B
S+4	Mem[SP]+4	Local Variable C

//Main code	for Ma	rieSimR
ORG	100	
Input		//Read input
Store	Ν	//N=input
Push	Ν	//Push N on stack (need to expand)
Call	Fib	//Call subroutine Fib
Рор	С	//Pop result into C (need to expand)
Load	С	//print result
Output		
Halt		//terminate program



## The values in stack at several important timestamps

(1) Right before calling Fib	Memory address	Used for
\$	Mem[SP]	Input N

(2) Right after calling Fib	Memory address	Used for
\$-1	Mem[SP]-1	Input N
\$	Mem[SP]	Return address
\$+1	Mem[SP]+1	Local variable I
\$+2	Mem[SP]+2	Local variable A
\$+3	Mem[SP]+3	Local variable B
\$+4	Mem[SP]+4	Local variable C

(3) Right before Fib return	Memory address	Used for
\$-1	Mem[SP]-1	Output Fib(N)
\$	Mem[SP]	Return address
\$+1	Mem[SP]+1	Local variable I
\$+2	Mem[SP]+2	Local variable A
\$+3	Mem[SP]+3	Local variable B
\$+4	Mem[SP]+4	Local variable C

(4) Right after Fib return	Memory address	Used for
\$	Mem[SP]	Output Fib(N)

## Method 4 (Using Recursive Function)

Stack Frame of Fib(N)

Right after calling Fib(N) and right before returning from Fib(N)

Location in Stack	Memory Address	Used for
\$-1	Mem[SP]-1	Input N / Output Fib(N)
\$	Mem[SP]	Return Address
\$+1	Mem[SP]+1	Input N-1 / Output Fib(N-1)
\$+2	Mem[SP]+2	Input N-2 / Output Fib(N-2)

//Main code	for Man	rieSimEx
ORG	100	
Input		//Read input
Store	Ν	//N=input
Push	Ν	//Push N on stack (need to expand)
JnS	Fib	//Call subroutine Fib
Рор	С	<pre>//Pop result into C (need to expand)</pre>
Load	С	//print result
Output		
Halt		//terminate program

```
//Subroutine Fib
Fib, Limm -1
   Add
      $-1 // AC = N-1
   Skipcond 800 // if N > 1
   jump L1 // Done
   Store $+1 // Store N-1 to $+1
   IncSP 1 // Increase SP ($) by 1
   Call Fib // Call F(N-1)
   Limm -2 // AC = -2
   Add $-2 // AC = N-2
   Store $+1 // Store N-2 to $+1
   IncSP 1 // Increase SP ($) by 1
   Call Fib // Call F(N-2)
   IncSP -2 // Decrease SP ($) by 2 (restore SP)
   Load $+1 // AC = Fib(N-1)
   Add \pm 2 // AC = F(N-1) + F(N-2) = F(N)
   Store $-1 // store Fib(N) to $-1
L1, JR
//Global Variable Declarations
N, DEC 0 //N -- input to Fib
  DEC
              //f(N)-- output from Fib
С,
          0
```

### The values in stack at several important timestamps (1)

Push N //Push	N on stack (need to exp	pand)
<pre>(1) Right before calling Fib(N)</pre>	Memory address	Used for
\$	Mem[SP]	Input N
Call Fib //Call	subroutine Fib	
<pre>(2) Right after calling Fib(N)</pre>	Memory address	Used for
\$-1 \$+0 \$+1 \$+2	Mem[SP]-1 Mem[SP] Mem[SP]+1 Mem[SP]+2	Input N Return address of Fib(N)
Store \$+1 // Stor IncSP 1 // Incr	e N-1 to \$+1 ease SP (\$) by 1	
(3) Right before calling Fib(N-1)	Memory address	Used for
\$-2 \$-1 \$	Mem[SP]-2 Mem[SP]-1 Mem[SP]	Input N Return address of Fib(N) N-1
\$+1	Mem[SP]+1	

## The values in stack at several important timestamps (2)

(4) Right after Fib(N-1) return	Memory address	Used for
\$-2	Mem[SP]-2	Input N
\$-1	Mem[SP]-1	Return address
\$	Mem[SP]	Fib(N-1)
\$+1	Mem[SP]+1	

Limm	-2	// AC = -2
Add	\$-2	// AC = N-2
Store	\$+1	// Store N-2 to \$+1
IncSP	1	// Increase SP (\$) by 1

(5) Right before calling Fib(N-2)	Memory address	Used for
\$-3	Mem[SP]-3	Input N
\$-2	Mem[SP]-2	Return address
\$-1	Mem[SP]-1	Fib(N-1)
\$	Mem[SP]	N-2

(6) Right after Fib(N-2) return	Memory address	Used for
\$-3	Mem[SP]-3	Input N
\$-2	Mem[SP]-2	Return address
\$-1	Mem[SP]-1	Fib(N-1)
\$	Mem[SP]	Fib(N-2)

## The values in stack at several important timestamps (3)

(6) Right after Fib(N-2) return	Memory address	Used for
\$-3	Mem[SP]-3	Input N
\$-2	Mem[SP]-2	Return address
\$-1	Mem[SP]-1	Fib(N-1)
\$	Mem[SP]	Fib(N-2)

IncSP	-2	// Decrease SP (\$) by 2 (restore SP)
Load	\$+1	// AC = Fib(N-1)
Add	\$+2	// AC = F(N-1) + F(N-2) = F(N)
Store	\$-1	// store Fib(N) to \$-1

(7) Right before Fib(N) return	Memory address	Used for
\$-1	Mem[SP]-1	Fib(N)
\$	Mem[SP]	Return address
\$+1	Mem[SP]+1	Fib(N-1)
\$+2	Mem[SP]+2	Fib(N-2)

#### L1, JR

(8) Right after Fib(N) return	Memory address	Used for
\$	Mem[SP]	Fib(N)

Pop C //Pop result into C (need to expand)